

INFORMATION DISCLOSURE CITATION				Attorney Docket No.: 046124-5287		Application No.: 10/821,272	
(Use several sheets if necessary)				PAGE 1 OF 1			
PTO Form-1449				Applicant(s): Seiichiro MIZUNO et al.		Filing Date: April 9, 2004	
						Group: 2821 Unassigned	

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U.S. PATENT DOCUMENTS						
Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
TV	5,898,334	April 27, 1999	Fairgrieve	37	411	May 12, 1997

FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Sub Class	Translation	
						Yes	No
TV	JP 63-251945	October 19, 1988	Japan	 	 	Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
TV	Nagahori <i>et al.</i> ; "An Analog Front-End Chip Set Employing an Electro-Optical Mixed Design on SPICE for 5-Gb/s/ch Parallel Optical Interconnection"; IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, December 2001; pp. 1984-1991.

Examiner TUYET VO	Date Considered 8/5/05
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

PTO Form 1449

Attorney Docket No.

046124-5287

Serial No.

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Applicants

Seiichiro MIZUNO, et al.

Filing Date

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*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub Class	Translation	
						YES	NO
TV	9-83442 0	March 28, 1997	Japan			Abstract	
	6-45675	February 18, 1994	Japan			Abstract	
	2001-36187	February 9, 2001	Japan			Abstract	
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	63-67791 0	March 26, 1988	Japan			Abstract	
	58-182144	October 25, 1983	Japan			Abstract	
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	2002-64433 0	February 28, 2002	Japan			Abstract	

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